Faculty Profile

Name	Mr. HARSH SHARADKUMAR THAKAR
Designation	ASSISTANT PROFESSOR
Department	ELECTRONICS & TELECOMMUNICATION
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Educational Qualifications

Degree	University/Institution	Year of Passing
M.E. (ELECTRONICS-VLSI)	BHARATI VIDYAPEETH	2008
B.E.(INDUSTRIAL ELECTRONICS)	PUNE UNIVERSITY	1998

Professional Experience

Sr. No.	Designation & Institute	Duration
1	Assistant Professor, P.I.C.T. , PUNE	2014 - Present
2	Assistant Professor, SINHGAD INSTITUTES	2005 - 2014

Research Publications

Sr. No.	Title	Publication/Conference
1)	IOT Based Energy Saving in E&TC Department	International Journal of Research Publication and Reviews, Vol 4, no 5, pp 5274-5279 May 2023
2)	Signal Processing on FPGA Using Hardware Co-Simulation	International Journal of Research Publication and Reviews, Vol 4, no 5, pp 6808-6811, May 2023
3)	FPGA Prototyping of 8-Bit Trellis Encoder & IC Design using Cadence	International Journal of Research Publication and Reviews, Vol 4, no 5, pp 4527-4532, May 2023
4)	Contrasting approaches for Vedic Multiplication using Xilinx ISE and Cadence	International Conference on Information and Communication Technology for Intelligent Systems

		Online ISBN : 978-981-97-6684-0
5)	Quantum Cryptanalysis: Analyzing Shor's Algorithm and its Impact on RSA Security	5th International Conference on Recent Trends in Machine Learning, IoT, Smart Cities and Applications **eBook ISBN :** 978-981-97-8861-3

Books Published

Sr. No.	Title	Publisher & Year
1)	VLSIDT (ISBN : N4234 978-93-5164-699-0)	TECH-MAX PUB. ,2019

Conferences / Seminars Attended

Sr. No.	Conference/Seminar	Year
1)	AICTE Sponsored : VLSI TO SYSTEM DESIGN : SILICON TO END APPLICATION APPROACH : 31-7-23 TO 4-8-23	2023
2)	Future Tech Revealed: Semiconductors, Quantum Computing, and Data Storage Insights : 20-1-25 TO 25-1-25 : COEP Technical University	2025

Awards & Recognitions

Areas of Interest

Sr. No.	Area
1)	Very Large Scale Integration (V.L.S.I.)
2)	Power Electronics
3)	VLSI EDA Tools : XILINX ISE , XILINX VIVADO , MicroWind , CADENCE
4)	Circuit Simulations Using : MultiSim , MATLAB SIMULINK